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PATENT  
Attorney Docket No.: 015114-053500US  
Client Reference No.: A618

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

ANDREW CROSLAND et al.

Application No.: 09/880,734

Filed: June 12, 2001

For: EMBEDDED PROCESSOR  
WITH WATCHDOG TIMER  
FOR PROGRAMMABLE LOGIC

Examiner: Unassigned

Art Unit: 2819

INFORMATION DISCLOSURE  
STATEMENT  
UNDER 37 CFR §1.97 and §1.98

Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

The references cited on attached form PTO/SB/08A and PTO/SB/08B are being called to the attention of the Examiner. Copies of the references are enclosed. It is respectfully requested that the cited references be expressly considered during the prosecution of this application, and the references be made of record therein and appear among the "references cited" on any patent to issue therefrom.

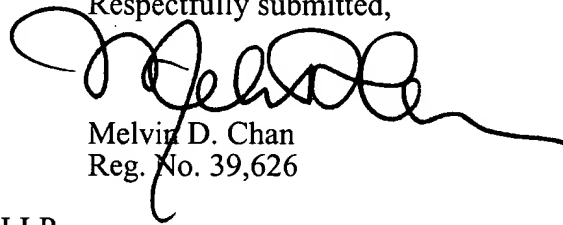
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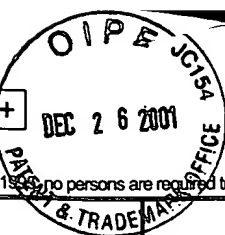


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Substitute for form 1449B/PTO

# **INFORMATION DISCLOSURE STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Sheet

1

of

1

## **Complete if Known**

Application Number	09/880,734
Filing Date	June 12, 2001
First Named Inventor	Crosland, Andrew
Group Art Unit	2819
Examiner Name	Unassigned
Attorney Docket Number	015114-053500US

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## **U.S. PATENT DOCUMENTS**

Examiner Initials *	Cite No. <sup>1</sup>	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code <sup>2</sup> (if known)			

## **FOREIGN PATENT DOCUMENTS**

Examiner Initials *	Cite No. <sup>1</sup>	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
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## **OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS**

Examiner Initials *	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
	01	"Triscend E5 Configurable System-on-Chip Family," Product Description from Triscend Corporation, January, 2000 (Version 1.00), pp. i-ii and 1-90.	
	02	"Motorola Technical Developments," Magazine of Motorola, Inc., Vol. 39, September 1999, pp. i-vii and 77-80.	
	03	"AT94K Series Field Programmable System Level Integrated Circuit," Advance Information Brochure of Atmel Corporation, December 1999, 6 pages.	
	04	"CS2000 Reconfigurable Communications Processor Family Product Brief," Advance Product Information from ChameleonSystems, Inc., 2000, pages 1-8	
	05	"Wireless Base Station Design Using Reconfigurable Communications Processors," Wireless Base Station White paper from ChameleonSystems, Inc., 2000, pages 1-8.	

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<sup>1</sup> EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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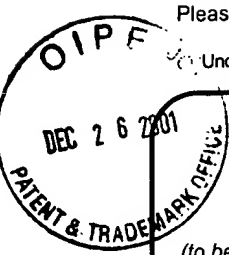
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<b>TRANSMITTAL FORM</b> (to be used for all correspondence after initial filing)	Application Number	09/880,734
	Filing Date	June 12, 2001
	First Named Inventor	Crosland, Andrew
	Group Art Unit	2819
	Examiner Name	Unassigned
Total Number of Pages in This Submission	Attorney Docket Number	015114-053500US

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Signature	
Date	December 26, 2001

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